

AKIMOTO *et al.*, SN 09/975,934RCE
Amdt. dated 3 August 2004
Reply to OA dated 3 May 2004

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IN THE SPECIFICATION:

Page 12, line 25 through page 13, line 15:

When an image signal is written in the display pixel, the moving image vertical direction selecting circuit 52 selects an address in the row direction and the moving image horizontal direction selecting circuit 44 selects an address of the moving image in the selected row. As a result, the AND gate circuit 47 of the selected display pixel is turned on and the connected TFT switch 48 is turned on. By ~~A/D~~ D/A converting the moving image data, the moving image signal output circuit 43 generates a signal voltage to be inputted to each of the selected display pixels and applies the signal voltage to the signal line 45. The signal voltage is inputted to the TN liquid crystal capacitor 49 via the TFT switch 48. Since the signal writing method of the still image is similar to that of the moving image, its description is omitted here.

Page 16, lines 12-21:

An image signal accuracy of about 6 bits is requested in the moving image and that of about 8 bits is required in the still image. Consequently, when the accuracy of the ~~A/D~~ D/A converter of the moving image signal output circuit 43 and that of the still image signal output circuit 41 are changed to 6 bits and 8 bits, respectively, the bit accuracy of the moving image signal output circuit 43 which is requested to operate at higher speed becomes lower, so that designing of the ~~A/D~~ D/A converter is facilitated.

Page 18, lines 16-22:

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In case of using the two-gradation still image signal output circuit 63, an ~~A/D~~ D/A converter is unnecessary so that power consumption is very small. For image data using monochromatic color or only multicolors of 8 colors, the power source of the still image signal output circuit 41 is temporarily stopped, thereby enabling the power consumption to be reduced.